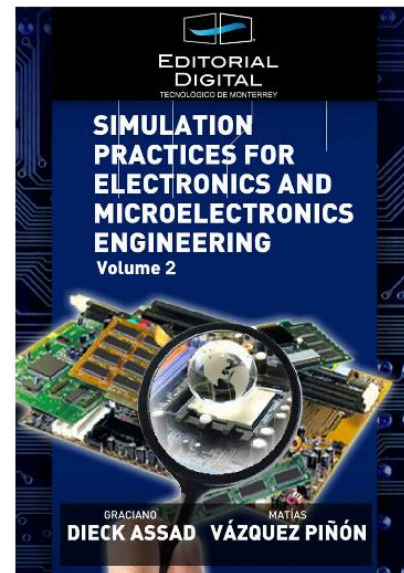


PUBLICACIÓN DE eBook SOBRE SIMULACIÓN PARA PROFESIONAL Y POSGRADO 2013

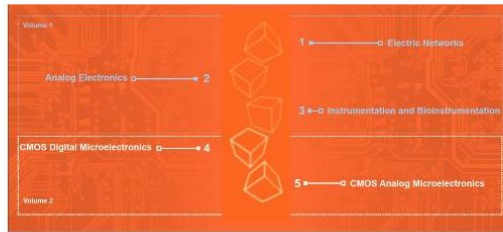
[2013] Vázquez M. y Dieck-Assad G., “**Simulation Practices for Electronics Engineering; Vol II: Microelectronics and Integrated Circuits Design**”, eBook publicado en por la Editora Digital del ITESM, ISBN-978-607-501-299-5, Abril de 2013.

The screenshot shows the Amazon Kindle Store interface. At the top, there's a navigation bar with the Amazon logo, search bar, and 'Valentine's Day deals'. Below that, a secondary navigation bar lists various categories like 'Buy a Kindle', 'Kindle eBooks', etc. The main content area features the book's cover on the left, which includes the title 'SIMULATION PRACTICES FOR ELECTRONICS AND MICROELECTRONICS ENGINEERING Volume 2' and authors 'GRACIANO DIECK ASSAD' and 'MATIAS VÁZQUEZ PIÑÓN'. To the right of the cover, the book title is repeated, followed by the authors' names and a 'Kindle Edition with Audio/Video' badge. A price box shows the digital list price as \$9.99 and the current price as \$7.62, with a 'Buy now with 1-Click' button. Below the price, there are buttons for 'Give as a Gift' and 'Add to List'. At the bottom of the page, there are social sharing options and a link to 'Enter a promotion code or Gift Card'.

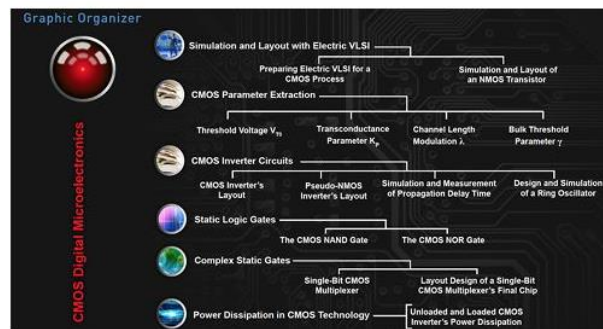
The screenshot shows the 'Table of Contents' section of the eBook. It lists various chapters and sections, including 'Cover', 'About this ePub', 'About the authors', 'Concept map', 'ePub introduction', and 'Chapter 4. CMOS Digital Microelectronics Si...'. Under 'Chapter 4', there are sub-sections for 'Introduction', '4.1 Simulation and Layout with Electric VLSI', '4.1.1 Preparing Electric VLSI for a CMOS pr...', '4.1.2 Simulation and layout of an NMOS Tr...', '4.2 CMOS Parameter Extraction', and '4.3 CMOS Inverter Circuits'.



Concept map



Chapter 4. CMOS Digital Microelectronics Simulation Practices



Chapter 5. CMOS Analog Microelectronics

